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Number of Pages (including this page)

Date: November 17, 2004
To: Thao P. Le - 2818
Location: United States Patent and Trademark Office
Fax No.: (703) 872-9306
From: Daniel D. Hill - 35,895
Subject: 10/716,956- Alexander B. Hoefler

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MESSAGE:

Enclosed herewith, please find a RESPONSE TO RESTRICTION REQUIREMENT for filing in the below-identified application.

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

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| 1. | x | 1 page Facsimile Cover Sheet |
| 2. | x | 2 page Response to Restriction Requirement |

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ON: 11/17/04
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PLEASE GIVE THESE PAPERS TO:

EXAMINER: Thao P. Le
GROUP ART UNIT: 2818
SERIAL NO.: 10/716,956
FILED: NOVEMBER 19, 2003
INVENTOR: ALEXANDER B. HOEFLER

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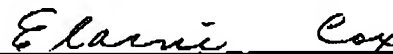
APPLICANT(S) Alexander B. Hoefler GROUP ART UNIT: 2818
APPLN. NO.: 10/716,956 EXAMINER: Thao P. Le
FILED: November 19, 2003
TITLE: MULTI-BIT NON-VOLATILE INTEGRATED CIRCUIT MEMORY
AND METHOD THEREFOR

Certificate of Transmission under 37 CFR 1.8

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on

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RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to an Office Action mailed November 2, 2004 setting a period of
response of one month.

If Applicant has overlooked any additional fees, or if any overpayment has been made,
the Commissioner is hereby authorized to credit or debit Deposit Account 503079.